APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 01-29-2004
Application Type:: Regular

Subject Matter:: Utility

Title:: Methods of Reducing the

Susceptibility of PLD Designs to

Single Event Upsets

Attorney Docket Number:: X-1281-1P US

Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 12
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor

Primary Citizenship Ctry:: US

Status:: Full Capacity

Given Name:: Eric Middle Name:: R.

Family Name:: Keller

Street:: 4619 Starboard Drive

City:: Boulder
State or Province:: Colorado

Postal or Zip Code:: 80301

Applicant Authority Type:: Inventor Primary Citizenship Ctry:: India

Status:: Full Capacity

Given Name:: Prasanna

Family Name:: Sundararajan

Street:: 500 West Middlefield Road, Apt 164

City:: Mountain View

State or Province:: California

Postal or Zip Code:: 94043

Applicant Authority Type:: Inventor

Primary Citizenship Ctry:: US

Status:: Full Capacity

Given Name:: Stephen

Middle Name:: M.

Family Name:: Trimberger

Street:: 1261 Chateau Drive

City:: San Jose
State or Province:: California

Postal or Zip Code:: 95120

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer		
Number::	24309	

DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Applic.	CIP	10/603,734	06-24-03

ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.

Street:: 2100 Logic Drive

City:: San Jose
State or Province:: California

Postal or Zip Code:: 95124